



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Nafea Bishara
Serial No. : 10/649,187
Filed : August 26, 2003
Title : ALIGNING IP PAYLOADS ON MEMORY BOUNDARIES FOR IMPROVED PERFORMANCE AT A SWITCH

Art Unit : 2185
Examiner : John A. Lane
Conf. No. : 5250

MAIL STOP RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

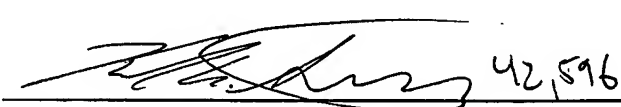
INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: 1-2-07


For Mark D. Kirkland
Reg. No. 40,048

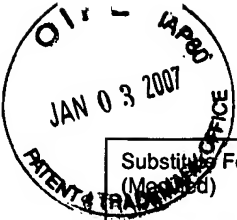
Fish & Richardson P.C.
500 Arguello Street, Suite 500
Redwood City, California 94063
Telephone: (650) 839-5070
Facsimile: (650) 839-5071

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Substitute Form PTO-1449 (Mandatory)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 13361-051001	Application No. 10/649,187
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Nafea Bishara	
		Filing Date August 26, 2003	Group Art Unit 2185

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AH							
	AI							
	AJ							
	AK							
	AL							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AM	IEEE Computer Society, "IEEE Standard for Information Technology – Telecommunications and Information Exchange Between Systems – Local and Metropolitan Area Networks – Specific Requirements – Part 3: Carrier Sense Multiple Access with Collision Detection (CSMA/CD) Access Method and Physical Layer Specifications", IEEE Std 802.3 – 2002, Sections 1-3
	AN	Trinh, Linh, "TCP/IP Sniffer Designs Teaches Basics of Embedded Ethernet," Electronic Design Home, ID #2099, April 15, 2002.
	AO	
	AP	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	